

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10808341			
<b>Filing Date:</b>	25-Mar-2004			
<b>Title of Invention:</b>	DSP (digital signal processing) architecture with a wide memory bandwidth and a memory mapping method thereof			
<b>First Named Inventor/Applicant Name:</b>	Hyun-kyu Yun			
<b>Filer:</b>	Patrick J. Stanzione/Iiona Wormack			
<b>Attorney Docket Number:</b>	101-1019			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Claims in excess of 20	1202	6	50	300
Independent claims in excess of 3	1201	2	200	400
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				700